Inventors: Krishnamurty vaidyan et al Attorney Docket: USD10722 Title: Programmable Délay Indexed Data Path Register File For Array Processing Contact: Dicran Halajian (914) 333-9607

1/5

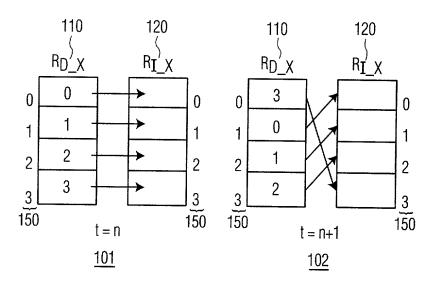


FIG. 1

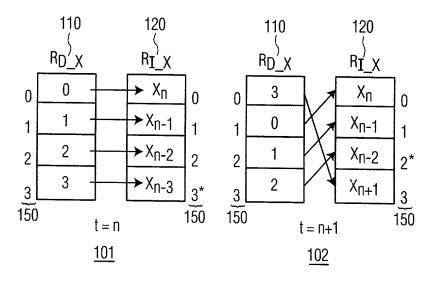


FIG. 1A

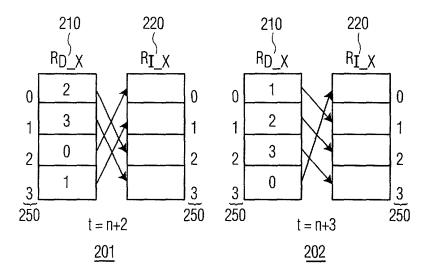


FIG. 2

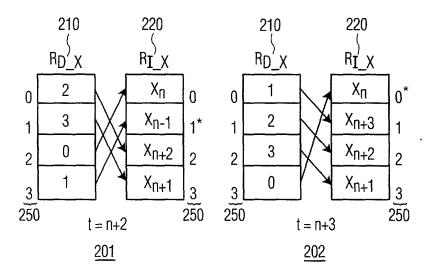
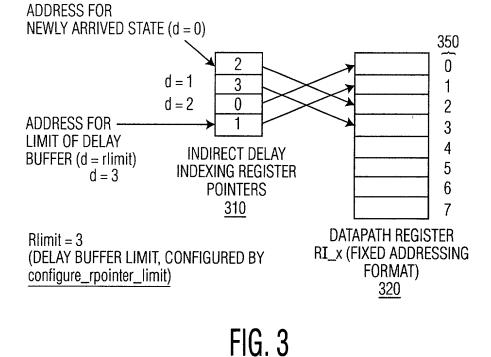


FIG. 2A



the half that the the man that

ADDRESS FOR NEWLY ARRIVED STATE (d = 0) 450 0 d = 12 1 3 d = 22 ADDRESS FOR -3 LIMIT OF DELAY 4 **INDIRECT DELAY** BUFFER (d = rlimit) 5 **INDEXING REGISTER** d = 3**POINTERS** 6 410 7 **DATAPATH REGISTER** Rlimit = 3RI_x (FIXED ADDRESSING (DELAY BUFFER LIMIT, CONFIGURED BY FORMAT) configure_rpointer_limit) 420

FIG. 4

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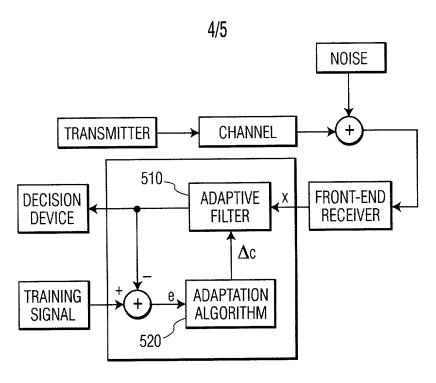


FIG. 5

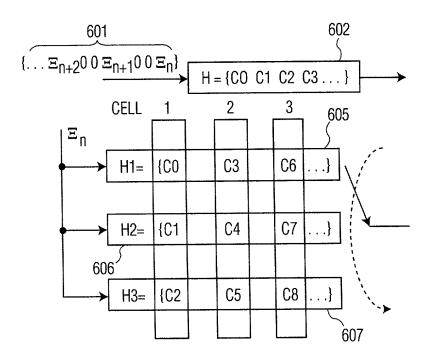


FIG. 6

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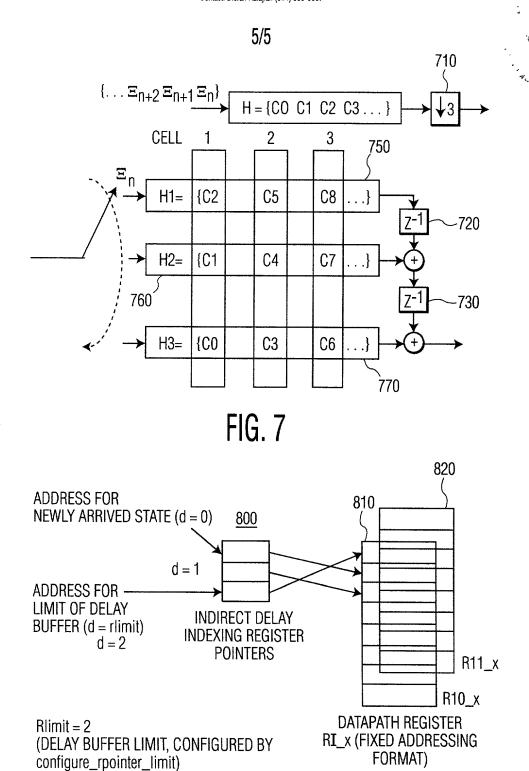


FIG. 8